

IN THE CLAIMS:

1. (Currently amended) An active matrix type electroluminescence display device comprising:

a plurality of display pixels arranged in a matrix of rows and columns, each of said display pixels including an electroluminescence element to which one end of a capacitor capacitance for maintaining a voltage corresponding to a display signal is connected; and

a plurality of capacitor capacitance-lines extending in each row and connected to and shared by the other end of said capacitor capacitance-of said display pixels; wherein

a constant voltage is supplied from both ends of said capacitor capacitance-lines;

one of said plurality of capacitor lines is connected to a gate of a driver transistor which drives the electroluminescence element; and

said plurality of capacitor lines extend along a row direction.

2. (Currently amended) An active matrix type electroluminescence display device comprising:

a plurality of display pixels, each including an electroluminescence element, arranged in a matrix of rows and columns, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to a select signal, a capacitor capacitance-having one end connected to the source of the first thin film transistor and for maintaining a voltage corresponding to said display signal, and a second thin film transistor for driving said electroluminescence element based on said display signal;

a plurality of first capacitor capacitance-lines, each extending for a row and connected to and shared by the other end of a capacitor capacitance-of said display pixels;

a second capacitor capacitance-line connected to first ends of said plurality of first capacitor capacitance-lines;

a third capacitor capacitance-line connected to second ends of said plurality of first capacitor capacitance-lines; wherein

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said second and third capacitor capacitance-lines are connected to a common constant voltage source, and said constant voltage is supplied to said first ends and said second ends of said plurality of first capacitor capacitance-lines through said second and third capacitor capacitance-lines.

3. (Currently amended) The device of claim 2, wherein

said second capacitor capacitance-line extends in a column direction on one side of an area in which said plurality of display pixels are arranged in a matrix, and

said third capacitor capacitance-line extends in a column direction on the other side of the area in which said plurality of display pixels are arranged in a matrix.

4. (Currently amended) An active matrix type electroluminescence display device comprising:

a plurality of display pixels, each including an electroluminescence element, arranged in a matrix of rows and columns, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to a select signal, a capacitor capacitance-having one end connected to the source of the first thin film transistor and for maintaining a voltage corresponding to said display signal, and a second thin film transistor for driving said electroluminescence element based on said display signal:

a plurality of first capacitor capacitance-lines, each extending for a row and connected to and shared by the other end of a capacitor capacitance-of said display pixels;

a second capacitor capacitance-line connected to first ends of said plurality of first capacitor capacitance-lines;

a third capacitor capacitance-line connected to second ends of said plurality of first capacitor capacitance-lines; and

wherein a said constant voltage is supplied to said first ends and second ends or said plurality of first capacitor capacitance-lines through said second and third capacitor capacitance-lines.

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5. (Currently amended) The device of claim 4, wherein

said second capacitor capacitance-line extends in a column direction on one side of an area in which said plurality of display pixels are arranged in matrix, and

said third capacitor capacitance-line extends in a column direction on the other side of the area in which said plurality of display pixels are arranged in matrix.

6. (Currently amended) The device of claim 1 comprising:

a second capacitor capacitance-line connected to first ends of said plurality of capacitor capacitance-lines;

a third capacitor capacitance-line connected to second ends of said plurality of capacitor capacitance-lines; and

wherein said constant voltage is supplied to said first ends and second ends or said plurality of capacitor capacitance-lines through said second and third capacitor capacitance lines.

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